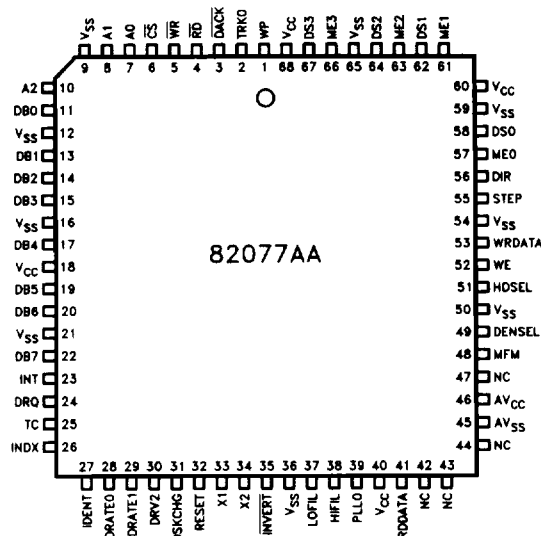# intel®

82077AA
CHMOS SINGLE-CHIP FLOPPY DISK CONTROLLER

- **Single-Chip Floppy Disk Solution**
  - **100% PC AT* Compatible**
  - **100% PS/2* Compatible**
  - **100% PS/2 Model 30 Compatible**
  - **Integrated Drive and Data Bus Buffers**
- **Integrated Analog Data Separator**
  - **250 Kbits/sec**
  - **300 Kbits/sec**
  - **500 Kbits/sec**
  - **1 Mbits/sec**
- **High Speed Processor Interface**
- **Perpendicular Recording Support**

- **Integrated Tape Drive Support**
- **12 mA Host Interface Drivers, 40 mA Disk Drivers**
- **Four Fully Decoded Drive Select and Motor Signals**
- **Programmable Write Precompensation Delays**
- **Addresses 256 Tracks Directly, Supports Unlimited Tracks**
- **16 Byte FIFO**
- **68-Pin PLCC**
  (See Packaging Spec., Order #240800, Package Type N)

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The 82077AA floppy disk controller has completely integrated all of the logic required for floppy disk control. The 82077AA, a 24 MHz crystal, a resistor package and a device chip select implements a PC AT or PS/2 solution. All programmable options default to compatible values. The dual PLL data separator has better performance than most board level/discrete PLL implementations. The FIFO allows better system performance in multi-master systems (e.g. PS/2, EISA). The 82077AA is available in three versions—82077AA-5, 82077AA and 82077AA-1. 82077AA-1 has all features listed in this data sheet. It supports both tape drives and 4 Mb floppy drives. The 82077AA supports 4 Mb floppy drives and is capable of operation at all data rates through 1 Mbps. The 82077AA-5 supports 500/300/250 Kbps data rates for high and low density floppy drives.

The 82077AA is fabricated with Intel's CHMOS III technology and is available in a 68-lead PLCC (plastic) package.

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Figure 1. 82077AA Pinout

*PS/2 and PC AT are trademarks of IBM.

The complete document for this product is available from Intel's Literature Center at 1-800-548-4725.